The Devito DSL and Compiler Framework: From Symbolic PDEs to HPC Code

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MS307: PDE Simulations with High-Productivity Languages at the Dawn of Exascale, SIAM CSE23, Amsterdam
Scientific simulations are demanding

⚠️ Very complex to model (complicated PDEs, BCs, external factors, complex geometries)

✅ Software offering high-level, high-productivity DSLs
✅ Let domain experts navigate their design space

⚠️ Resource-demanding $(O(10^3)$ FLOPs per loop iteration, high memory pressure, 3D grids with $> 10^9$ grid points, often $O(10^3)$ time steps, inverse problems, $\approx O(\text{billions})$ TFLOPs. Which means days, or weeks, or months on supercomputers!

✅ Offer automated optimisations and efficient codegen for HPC workloads
✅ Higher resolution in space and time opens up compelling new applications
✅ Unlocks ever-increasing application value
void kernel(...) {

...
Introducing Devito

- Devito is a **DSL and compiler framework** for finite difference and stencil computations

- **Solving PDEs** using the **finite-difference method for structured grids** (but not limited to this!)

- Users model in the **high-level DSL** using symbolic math abstraction, and the **compiler auto-generates HPC optimized code**

- Inter(-national, -institutional,-disciplinary), lots of users from academia and industry

- Real-world problem simulations! (CFD, seismic/medical imaging, finance, tsunamis)
Introducing Devito

- **Open source** - MIT lic. - Try now! https://github.com/devitocodes/devito

- **Compose with** packages from the Python ecosystem (e.g. PyTorch, NumPy, Dask, TensorFlow)

- Best practices in **software engineering**: extensive software testing, code verification, CI/CD, regression tests, documentation, tutorials and PR code review

- Actual compiler technology (not a S2S translator or templates!)

Adapted from Aryaman Sharda Cueto et.al. (2022)
An example from textbook maths to via Devito DSL

2D Heat diffusion modelling

\[
\begin{align*}
\frac{\partial^2 u}{\partial x^2}(x, y) + \frac{\partial^2 u}{\partial y^2}(x, y) &= \frac{\partial u}{\partial t}(x, y) \\
u(x, 0) &= 0 \\
u(0, y) &= 0 \\
u(x, 1) &= 0 \\
u(1, y) &= 0
\end{align*}
\]

from devito import Eq, Grid, TimeFunction, Operator, solve

# Define a structured grid
nx, ny = 10, 10
grid = Grid(shape=(10, 10))

# Define a field on the structured grid
u = TimeFunction(name='u', grid=grid, space_order=2)

# Define a forward time-stepping symbolic equation
eqn = Eq(u.forward, solve(eqn, u.forward))

# Define boundary conditions
x, y = grid.dimensions
t = grid.stepping_dim
bc_left = Eq(u[t + 1, 0, y], 0.)
bright = Eq(u[t + 1, nx-1, y], 0.)
bc_top = Eq(u[t + 1, x, ny-1], 0.)
bbottom = Eq(u[t + 1, x, 0], 0.)
eqns += [bc_left, bc_bottom, bc_right, bc_top]

# Define an Operator object
op = Operator(eqns)

# Compute for 3 timesteps
op.apply(time_M=3, dt=0.1)
Devito’s API is much richer though…

- Any PDE simulations
- Boundary conditions
- Sparse off-grid operations (interpolations)
- Subdomains
- Immersed boundaries (WIP)

Examples available with:
- **CFD** (convection/diffusion/cavity flow/shallow waters (tsunami) /Darcy flow)
- **Wave propagators** ((Visco-)Acoustic/Elastic, TTI)
- **Seismic/Medical Imaging** (FWI/RTM)
- **Finance**

Thanks to Ed Caunt
The Devito compiler automatically applies lots of optimisations

- Equations lowering (Input equations are lowered to stencil representations)
- Clustering (Group equations according to properties)
- Symbolic optimization (Apply arithmetic optimisations and loop transformations)
- Lowering expressions to an Iteration/Expression tree
- Apply shared- and distributed-memory Parallelism
- Use the IET to synthesize a C/C++ string
- JIT Compilation (C/C++ string -> kernel.c -> kernel.so)

- Loop Fusion
- Hoist and optimize Dimension-invariant sub-expressions
- Loop blocking
- Factorization
- Loop Fission
- Optimize powers
- Cross-Iteration redundancies elimination
- Eliminate redundant array expressions
- Common sub-expressions elimination

- Flush denormals
- Distributed Memory parallelism
- SIMD vectorization
- Shared memory parallelism
- Profiling instrumentation

+ advanced combinations of them!
+ heuristics to tune them more!

- Serial C/CPP code
- OpenMP parallel code
- MPI (+ OpenMP )
- OpenMP 5 GPU offloading via Clang
- OpenACC GPU offloading

Write once, Run everywhere!
Math-related optimisations -- Reducing OI/AI of stencil kernels

- **Isotropic Acoustic**
  Generally known, single scalar PDE, laplacian like, low cost

- **Isotropic Elastic**
  Coupled system of a vectorial and tensorial PDE, explosive source, increased data movement, first order in time, cross-loop data dependencies

- **Anisotropic Acoustic (aka TTI, Zhang-Louboutin variation)**
  Industrial applications, rotated laplacian, coupled system of two scalar PDEs, several variations based on variable or constant density

⚠️ Not the typical memory-bw bound stencils!
✅ Significantly reduced operational intensity!
Write once, run everywhere!

Devito offers automated MPI-openmp code generation, taking advantage of several optimised communication/computation patterns

User only has to use: “DEVITO_MPI=<mode> mpirun -n 2 python my_devitoscript.py”

Figure style influenced from Li et.al, ICPP 2021
Performance evaluation: Strong scaling on Archer2

- Archer2 HPE Cray EX Supercomputer
- 128 dual AMD EPYC 7742 64-core 2.25GHz nodes
- 8 NUMA regions per node (16 cores per NUMA region)
- HPE Slingshot interconnect with 200 Gb/s signalling
- 8 MPI-ranks per node and 16 openmp workers per MPI rank, total of 128 cores per node
- Strong scaling up to 16384 cores

Isotropic acoustic wave propagation kernel
shape=[1024 1024 1024], timesteps=512, with sponge-layers BCs
Performance evaluation: Strong scaling on Archer2

- Archer2 HPE Cray EX Supercomputer
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- HPE Slingshot interconnect with 200 Gb/s signalling
- 8 MPI-ranks per node and 16 openmp workers per MPI rank, total of 128 cores per node
- Strong scaling up to 16384 cores

Anisotropic acoustic wave propagation kernel (TTI) (Zhang/Louboutin), shape=[1024 1024 1024] timesteps=512, with sponge-layers BCs
Performance evaluation: GPU results

iso_acoustic performance [2020 -> 2023]

tti_fletcher performance [2020 -> 2023]

tti_selfadjoint performance [2020 -> 2023]

iso_acoustic single-node multi-GPU strong scaling [mode diag2]

tti_fletcher single-node multi-GPU strong scaling [mode diag2]

tti_selfadjoint single-node multi-GPU strong scaling [mode diag2]
Conclusions

• We presented the Devito DSL and Compiler framework for stencil computation for solving PDEs using the FD method on structured grids (but not limited to them!)

• The Devito compiler supports a great variety of optimisations for stencil kernels, and support for shared- and distributed memory parallelism…all that…automatically and automagically!

• Performance results on UK’s strongest SC show competitive strong scaling!

• Preliminary performance benchmarking on MPI+GPUs

• Future work:
  - further improve our MPI implementations for better scaling
  - Multi-node multi-GPU
References


Appendix
I'm still waiting for the day that I will actually use

### Schrödinger Equation
\[ \frac{-\hbar^2}{2m} \nabla^2 \psi + V(\vec{r}) \psi = i\hbar \frac{\partial \psi}{\partial t} \]

### Poisson Equation
\[ \nabla^2 u = \rho(\vec{r}) \]

### Laplace Equation
\[ \nabla^2 u = 0 \]

### Wave Equation
\[ \frac{\partial^2 u}{\partial t^2} = \frac{1}{\rho} \frac{\partial^2 u}{\partial x^2} \]

### Diffusion Equation
\[ \frac{\partial u}{\partial t} = \frac{D}{\rho} \frac{\partial^2 u}{\partial x^2} \]

### User writes in
```python
void kernel(...) {
    # ...
}
```

### Textbook math:
\[ m \frac{\partial^2 u}{\partial t^2} + \eta \frac{\partial u}{\partial t} - \Delta u = 0 \]

### User writes in
```python
eqn = M * u.dt2 + eta * u.dt - u.laplace
```

### High-level abstraction!
void kernel(...) {
...
<impenetrable code with aggressive performance optimizations,
manually applied, full-time human resources, less
reproducibility, debugging nightmares>
...}
Groups of expressions, Cluster-level
(Cluster([Eq(u[t1, x + 2, y + 2], u[t0, x + 1, y + 2] / h_x**2 + 2.0*u[t0, x + 2, y + 2] / h_x**2 + u[t0, x + 3, y + 2] / h_x**2 + u[t0, x + 2, y + 1] / h_y**2 + 2.0*u[t0, x + 2, y + 2] / h_y**2 + u[t0, x + 2, y + 3] / h_y**2 + u[t0, x + 2, y + 3] / h_y**2 + 1]),

from devito import Eq, Grid, TimeFunction, Operator

grid = Grid(shape=(4, 4))
u = TimeFunction(name='u', grid=grid, space_order=2)
u.data[:] = 1

eq = Eq(u.forward, u.laplace + 1)
op = Operator(eq)
op.apply(time_M=3)

Groups of expressions, Cluster-level (Optimized)
(Cluster([Eq(r0, 1/(h_x*h_x))
          Eq(r1, 1/(h_y*h_y))]),
Cluster([Eq(r2, -2.0*u[t0, x + 2, y + 2])
          Eq(u[t1, x + 2, y + 2], r0*r2 + r0*u[t0, x + 1, y + 2] + r0*u[t0, x + 3, y + 2] + r1*r2 + r1*u[t0, x + 2, y + 1] + r1*u[t0, x + 2, y + 3] + 1)]))

<Callable Kernel>
<CallableBody <allocs=0, casts=0, maps=0> <unmaps=0, frees=0> 
<List (4, 0, 0)> 
<C.Comment /* Flush denormal numbers to zero in hardware */>
<C.Statement _MM_SET_DENORMALS_ZERO_MODE(_MM_DENORMALS_ZERO_ON)> 
<C.Statement _MM_SET_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_ON)> 
<C.Line >
<List (0, 2, 0)> 
<Expression r0 = 1/(h_x*h_x)> 
<Expression r1 = 1/(h_y*h_y)> 
[affine,sequential] Iteration time::time::(time_m, time_M, 1) 
<Section (section0)> 
<OverlappableHaloSpot(u)> 
<OmpRegion (1, 1, 0)> 
<CPragma #pragma omp parallel num_threads(nthreads)> 
<ParallelTree (0, 1, 0)> 
[affine,collapsed[1],parallel] Iteration x::x::(x_m, x_M, 1) 
[affine,parallel, vector-dim] Iteration y::y::(y_m, y_M, 1) 
<ExpressionBundle (2)> 
<Expression r2 = -2.0*u[t0, x + 2, y + 2]> 
<Expression u[t1, x + 2, y + 2] = r0*r2 + r0*u[t0, x + 1, y + 2] + r0*u[t0, x + 3, y + 2] + r1*r2 + r1*u[t0, x + 2, y + 1] + r1*u[t0, x + 2, y + 3] + 1>
int Kernel(const float h_x, const float h_y, struct dataobj *restrict u_vec, const int time_M, const int time_m, const int x_M, const int x_m, const int y_M, const int y_m)
{
    r0 = 1.0F/(h_x*h_x);
    r1 = 1.0F/(h_y*h_y);

    for (int time = time_m, t0 = (time)%(2), t1 = (time + 1)%(2); time <= time_M; time += 1, t0 = (time)%(2), t1 = (time + 1)%(2))
    {
        /* Begin section0 */
        for (int x = x_m; x <= x_M; x += 1)
        {
            for (int y = y_m; y <= y_M; y += 1)
            {
                r2 = -2.0F*u[t0][x + 2][y + 2];
                u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1][y + 2] + r0*u[t0][x + 3][y + 2] + r1*r2 + r1*u[t0][x + 2][y + 1] + r1*u[t0][x + 2][y + 3] + 1;
            }
        }
        /* End section0 */
    }
}
Mapping from IET level to c-code - Add denormals

```c
int Kernel(const float h_x, const float h_y, struct dataobj *restrict u_vec, const int time_M, const int time_m, const int x_M, const int x_m, const int y_M, const int y_m)
{
    /* Flush denormal numbers to zero in hardware */
    _MM_SET_DENORMALS_ZERO_MODE(_MM_DENORMALS_ZERO_ON);
    _MM_SET_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_ON));

    r0 = 1.0F/(h_x*h_x);
    r1 = 1.0F/(h_y*h_y);

    for (int time = time_m, t0 = (time)%(2), t1 = (time + 1)%(2); time <= time_M; time += 1, t0 = (time)%(2), t1 = (time + 1)%(2))
    {
        /* Begin section0 */
        for (int x = x_m; x <= x_M; x += 1)
        {
            for (int y = y_m; y <= y_M; y += 1)
            {
                r2 = -2.0F*u[t0][x + 2][y + 2];
                u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1][y + 2] + r0*u[t0][x + 3, y + 2] + r1*r2 + r1*u[t0][x + 2, y + 1] + r1*u[t0][x + 2, y + 3] + 1;
            }
        }
        /* End section0 */
    }
}```
int Kernel(...)
{
    /* Flush denormal numbers to zero in hardware */
    _MM_SET_DENORMALS_ZERO_MODE(_MM_DENORMALS_ZERO_ON);
    _MM_SET_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_ON);

    r0 = 1.0F/(h_x*h_x);
    r1 = 1.0F/(h_y*h_y);

    for (int time = time_m, t0 = (time)%2, t1 = (time + 1)%2; time <= time_M; time += 1,
        t0 = (time)%2, t1 = (time + 1)%2))
    {
        /* Begin section0 */
        #pragma omp parallel num_threads(nthreads)
        {
            #pragma omp for collapse(1) schedule(dynamic,1)
            for (int x = x_m; x <= x_M; x += 1)
            {
                #pragma omp simd aligned(u:32)
                for (int y = y_m; y <= y_M; y += 1)
                {
                    r2 = -2.0F*u[t0][x + 2][y + 2];
                    u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1, y + 2] + r0*u[t0][x + 3, y + 2] + r1*r2 + r1*u[t0][x + 2, y + 1] + r1*u[t0][x + 2, y + 3] + 1;
                }
            }
        }
        /* End section0 */
    }
}
Mapping from IET level to c-code  - Add parallelism

```c
int Kernel...
{
    /* Flush denormal numbers to zero in hardware */
    _MM_SET_DENORMALS_ZERO_MODE(_MM_DENORMALS_ZERO_ON);
    _MM_SET_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_ON);
    r0 = 1.0F/(h_x*h_x);
    r1 = 1.0F/(h_y*h_y);

    for (int time = time_m, t0 = (time)%2, t1 = (time + 1)%2; time <= time_M; time += 1,
     t0 = (time)%(2), t1 = (time + 1)%2)
    {
        /* Begin section0 */
        #pragma omp parallel num_threads(nthreads)
        {
            #pragma omp for collapse(1) schedule(dynamic,1)
            for (int x = x_m; x <= x_M; x += 1)
            {
                #pragma omp simd aligned(u:32)
                for (int y = y_m; y <= y_M; y += 1)
                {
                    r2 = -2.0F*u[t0][x + 2][y + 2];
                    u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1][y + 2] +
                    r0*u[t0][x + 3][y + 2] + r1*r2 + r1*u[t0][x + 2][y + 1] +
                    r1*u[t0][x + 2][y + 3] + 1;
                }
            }
        }
        /* End section0 */
    }
}
```
Pipeline for each target: CPU/OpenMP

```c
int Kernel(const float h_x, const float h_y, struct dataobj *restrict u_vec, const int time_M, const int time_m, const int x_M, const int x_m, const int y_M, const int y_m, const int nthreads, struct profiler *timers)
{
  float (*restrict u)[u_vec->size[1]][u_vec->size[2]] __attribute__((aligned (64))) = (float *)[u_vec->size[1]][u_vec->size[2]] u_vec->data;

  /* Flush denormal numbers to zero in hardware */
  _MM_SET_DENORMALS_ZERO_MODE(_MM_DENORMALS_ZERO_ON);
  _MM_SET_FLUSH_ZERO_MODE(_MM_FLUSH_ZERO_ON);

  float r0 = 1.0/(h_x*h_x);
  float r1 = 1.0/(h_y*h_y);

  for (int time = time_m, t0 = (time)%2, t1 = (time+1)%2; time <= time_M; time += 1, t0 = (time)%2, t1 = (time+1)%2)
  {
    /* Begin section0 */
    START_TIMER(section0)
    #pragma omp parallel num_threads(nthreads)
    {
      #pragma omp for collapse(1) schedule(dynamic,1)
      for (int x = x_m; x <= x_M; x += 1)
      {
        #pragma omp simd aligned(u:32)
        for (int y = y_m; y <= y_M; y += 1)
        {
          float r2 = -2.0F*u[t0][x + 2][y + 2];
          u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1][y + 2] + r0*u[t0][x + 3][y + 2] + r1*r2 + r1*u[t0][x + 2][y + 1] + r1*u[t0][x + 2][y + 3] + 1;
        }
      }
    }
    STOP_TIMER(section0,timers)
    /* End section0 */
  }

  return 0;
}
```
int Kernel(const float h_x, const float h_y, struct dataobj *restrict u_vec, const int time_M, const int time_m, const int x_M, const int x_m, const int y_M, const int y_m, const int deviceid, const int devicerm, struct profiler *timers)
{
    /* Begin of OpenACC setup */
    acc_init(acc_device_nvidia);
    if (deviceid != -1)
    {
        acc_set_device_num(deviceid,acc_device_nvidia);
    }
    /* End of OpenACC setup */

    float (*restrict u)[u_vec->size[1]][u_vec->size[2]] __attribute__ ((aligned (64))) = (float (*)[u_vec->size[1]][u_vec->size[2]]) u_vec->data;

#pragma acc enter data copyin(u[0:u_vec->size[0]][0:u_vec->size[1]][0:u_vec->size[2]])

    float r0 = 1.0F/(h_x*h_x);
    float r1 = 1.0F/(h_y*h_y);

    for (int time = time_m, t0 = (time)%2, t1 = (time + 1)%2; time <= time_M; time += 1, t0 = (time)%2, t1 = (time + 1)%2)
    {
        /* Begin section0 */
        START_TIMER(section0)

        #pragma acc parallel loop collapse(2) present(u)
        for (int x = x_m; x <= x_M; x += 1)
        {
            for (int y = y_m; y <= y_M; y += 1)
            {
                float r2 = -2.0F*u[t0][x + 2][y + 2];
                u[t1][x + 2][y + 2] = r0*r2 + r0*u[t0][x + 1][y + 2] + r0*u[t0][x + 3][y + 2] + r1*r2 + r1*u[t0][x + 2][y + 1] + r1*u[t0][x + 2][y + 3] + 1;
            }
        }
        STOP_TIMER(section0,timers)
        /* End section0 */

    #pragma acc exit data copyout(u[0:u_vec->size[0]][0:u_vec->size[1]][0:u_vec->size[2]])
    #pragma acc exit data delete(u[0:u_vec->size[0]][0:u_vec->size[1]][0:u_vec->size[2]])
    if (devicerm)
        return 0;
    }

Pipeline for each target: GPU/OpenACC
Fig. 6. Performance of tti on core for different architectures and grids.
No abstraction

\[ m \frac{\partial^2 u}{\partial t^2} + \eta \frac{\partial u}{\partial t} - \Delta u = 0 \]

```cpp
void kernel(...) {
    ...
    <impenetrable code with aggressive performance optimizations>
    ...
}
```

Devito DSL abstraction

\[ m \frac{\partial^2 u}{\partial t^2} + \eta \frac{\partial u}{\partial t} - \Delta u = 0 \]

```cpp
eqn = M * u.dt2 + eta * u.dt - u.laplace
void kernel(...) {
    {...}
}```
Modelling practical applications

- Stencils everywhere, not only though. What else?

- Remarkable amount of work in the past on optimizing stencils... (Parallelism, cache optimizations, accelerators)

- Sources injecting and receivers interpolating at sparse off-the-grid coordinates. **Non-conventional update patterns.**

- Usually their coordinates are not aligned with the computational grid. How do we iterate over them?

Off-the-grid operators (Source injection/Receiver interpolation)
If you do not use Devito (or other high-level tool)... good luck!
The cluster level

What data/metadata does a cluster hold?

• Expressions (Equations)
  \[
  \text{Cluster}\{\text{Eq}(u[t1, x + 2, y + 2], u[t0, x + 1, y + 2]/h_x^2 - 2.0*u[t0, x + 2, y + 2]/h_x^2 + u[t0, x + 3, y + 2]/h_x^2 + u[t0, x + 2, y + 1]/h_y^2 - 2.0*u[t0, x + 2, y + 2]/h_y^2 + u[t0, x + 2, y + 3]/h_y^2 + 1)\}
  \]

• IterationSpace
  \[
  \text{IterationSpace}\{\text{time}[0,0]<008>++, x[0,0]<008>++, y[0,0]<008>++\}
  \]

• Detect computational properties
  \[
  \text{<frozendict}\{\text{time}: \{\text{affine, sequential}\}, x: \{\text{affine, tilable, skewable, parallel}\}, y: \{\text{affine, tilable, skewable, parallel}\}\}
  \]

What data/metadata does a cluster hold?

• Relations
  \[
  \text{clusters[0].ispaces.relations} = \{(\text{time}, t), (\text{time}, x, y), (t, x, y), ()\}
  \]

• Sub-Iterators
  \[
  \text{<frozendict}\{\text{time}: (t0, t1), x: (), y: ()\}
  \]

• Directions
  \[
  \text{<frozendict}\{\text{time}: ++, x: ++, y: ++\}
  \]

• Dimensions
  \[
  \{t, y, \text{time}, x\}
  \]

Dummy cluster pass:

https://gist.github.com/georgebisbas/8115b94b86a3ffbc25e179f1e22c49e7
The cluster level

Optimized! 😊😊😊

float r0 = 1.0/(h_x*h_x);
float r1 = 1.0/(h_y*h_y);

for (int time = time_m, t0 = (time+1)%2; time <= time_M; time += 1, t0 = (time)%2, t1 = (time+1)%2)
{
    /* Begin section0 */
    START_TIMER
    for (int x = x_m; x <= x_M; x += 1)
    {
        #pragma omp simd aligned(u:32)
        for (int y = y_m; y <= y_M; y += 1)
        {
            float r2 = -2.0*u[t0, x+2, y+2];
            u[t1][x+2][y+2] = r0*r2 + r0*u[t0][x+1][y+2] + r0*u[t0][x+3][y+2] +
                             r1*r2 + r1*u[t0][x+2][y+1] + r1*u[t0][x+2][y+3] + 1;
        }
    }
    STOP_TIMER
    /* End section0 */
}

return 0;

What data/metadata does a cluster hold?

- **Expressions (Equations)**
  (Pdb) clusters[0]
  Cluster([Eq(r0, 1/(h_x*h_x))]
  Eq(r1, 1/(h_y*h_y))])
  (Pdb) clusters[1]
  Cluster([Eq(r2, -2.0*u[t0, x+2, y+2])]
  Eq(u[t1, x+2, y+2], r0*r2 + r0*u[t0][x+1, y+2] + r0*u[t0][x+3, y+2] +
     r1*r2 + r1*u[t0][x+2, y+1] + r1*u[t0][x+2, y+3] + 1))

- **IterationSpace**
  (Pdb) clusters[0].ispace
  IterationSpace[
  (Pdb) clusters[1].ispace
  IterationSpace[time[0,0]<960>++, x[0,0]<960>++, y[0,0]<960>++]

---

Group expressions into clusters
Add guards for conditional clusters
Analysis
Detect properties such as parallelism
Applying temporal-blocking

Temporal blocking (Time-Tiling):
- Space blocking but data reuse is extended to time-dimension.
- Update grid points in future where (space) and when (time) possible
- Rich literature, several variants of temporal blocking, shapes, schemes
  - Wave-front / Skewed (Approach followed in the paper)
  - Diamonds, Trapezoids, Overlapped, Hybrid models

Tanaka et.al. (2018)
Architecture of Devito
Experimental evaluation: the models

- **Isotropic Acoustic**
  Generally known, single scalar PDE, laplacian like, low cost

- **Isotropic Elastic**
  Coupled system of a vectorial and tensorial PDE, explosive source, increased data movement, first order in time, cross-loop data dependencies

- **Anisotropic Acoustic (aka TTI)**
  Industrial applications, rotated laplacian, coupled system of two scalar PDEs

Industrial-level, 512^3 grid points, 512ms simulation time, damping fields ABCs

Velocity field, TTI wave propagation after 512ms
Effects of Cache Behavior on Arithmetic Intensity
The Roofline model requires an estimate of total data movement. On cache-based architectures, the 3C's cache model highlights the fact that there can be more than simply compulsory data movement. Cache capacity and conflict misses can increase data movement and reduce arithmetic intensity. Similarly, superfluous cache write-allocations can result in a doubling of data movement. The vector initialization operation $x[i]=0.0$ demands one write allocate and one write back per cache line touched. The write allocate is superfluous as all elements of that cache line are to be overwritten. Unfortunately, the presence of hardware stream prefetched can make it very difficult to quantify how much beyond compulsory data movement actually occurred.
PDE Simulations with High-Productivity Languages at the Dawn of Exascale

We need software to answer two address (among others) two main concerns

- 🚧 Simulations are very complex to model (complicated PDEs, boundary conditions, external factors, complex geometries, need to model real-world applications as accurately as possible)
  ✓ **Software** offering high-level, high-productivity Domain Specific Languages

- 🚧 Resource-demanding $O(10^3)$ FLOPs per loop iteration, high memory pressure, 3D grids with $> 10^9$ grid points, often $O(10^3)$ time steps, inverse problems, $\approx O(\text{billions})$ TFLOPs. Which means days, or weeks, or months on supercomputers!
  ✓ **Software** offering automated optimisations and efficient codegen for HPC workloads towards Exascale
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